

**6U 6Slot VPX BackPlane**

**6U 卡 6 槽位 VPX 总线背板**

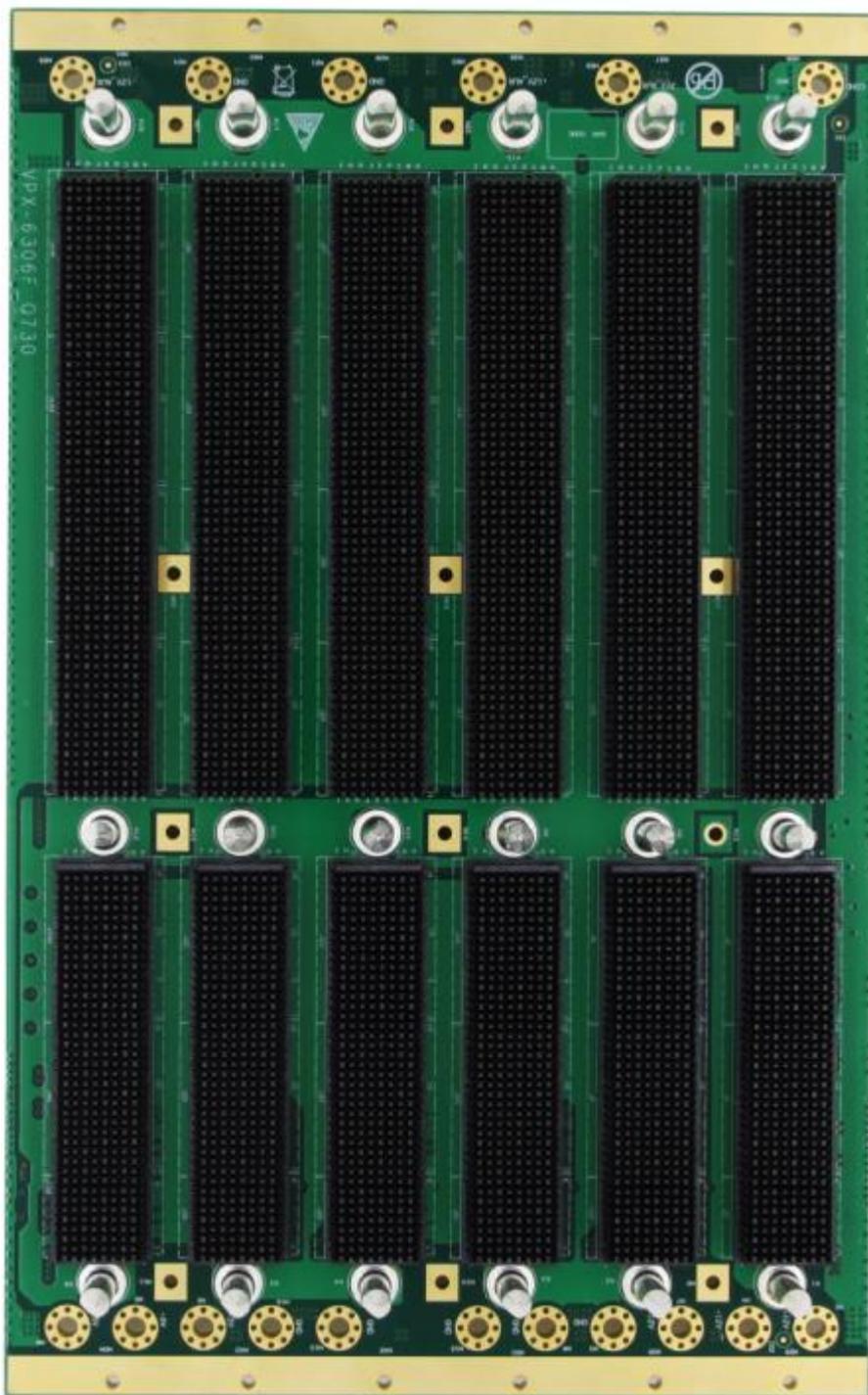
**1" Pitch**

**槽位间距 25.4mm**

**VPX6306F**

**用户手册**

VPX6306F 产品图片



**产品描述：**

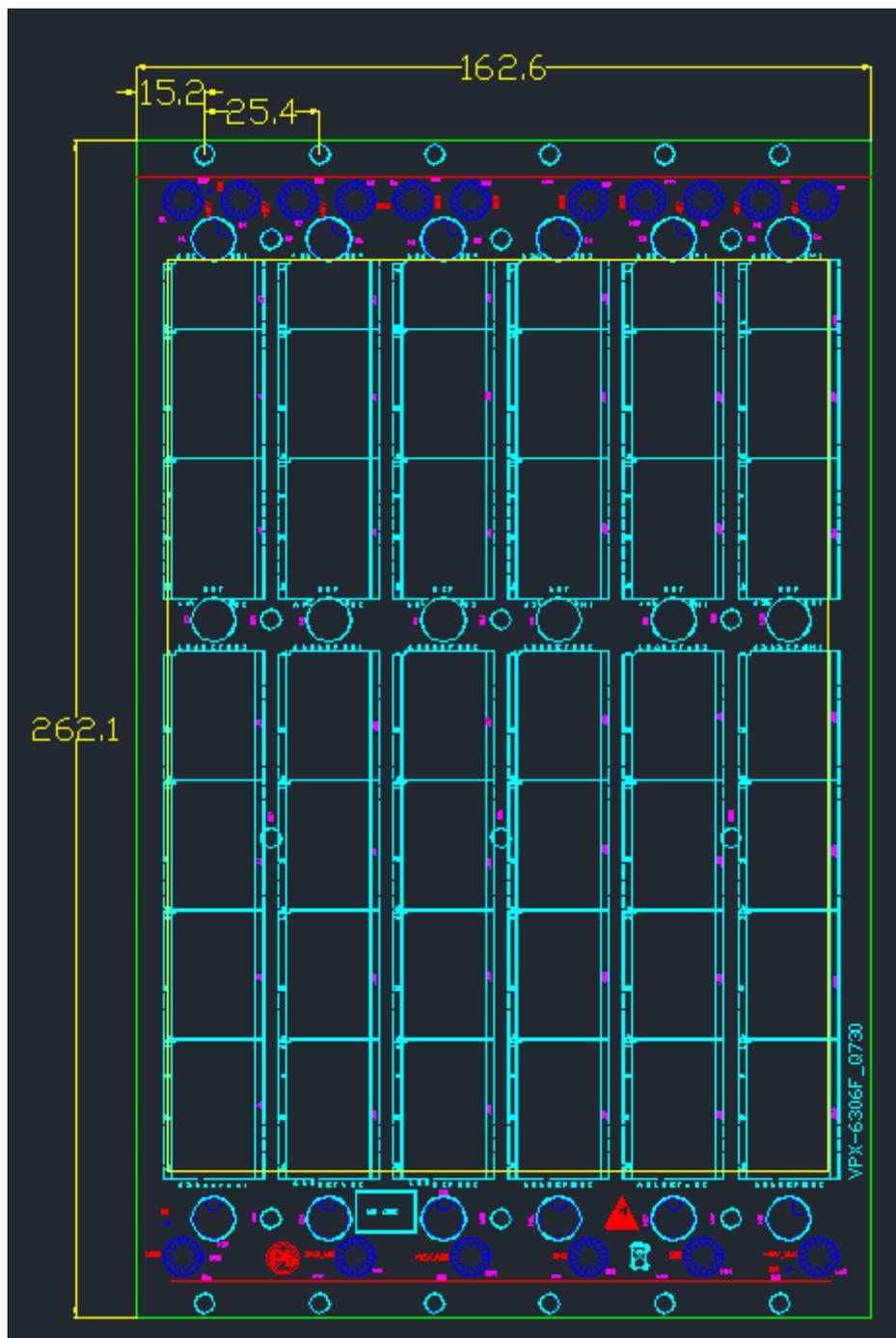
国控精仪的 6U 6 槽 OpenVPX 背板按规范提供集中交互、自互联拓扑结构，并额外提供一条链状互联通道。本产品 PCB 为分布式自互联拓扑结构提供 3.125G-6.250G 的性能。可根据客户要求增加后出线功能。

**产品特点：**

- ◆ 符合 ANSI/VITA 65-2010 规范；
- ◆ 符合 ANSI/VITA 46 规范；

- ◆ 可插入 6U VPX 板卡；
- ◆ 6 槽位设计，第 6 槽位为交换板槽位；
- ◆ 各槽位均提供静电接地保护；
- ◆ 集中交互、自互联、链状互联三种拓扑结构；
- ◆ 16 层 PCB 板设计；5.4mm 板厚；FR-4 材质；
- ◆ 支持双电源、大电流接入；

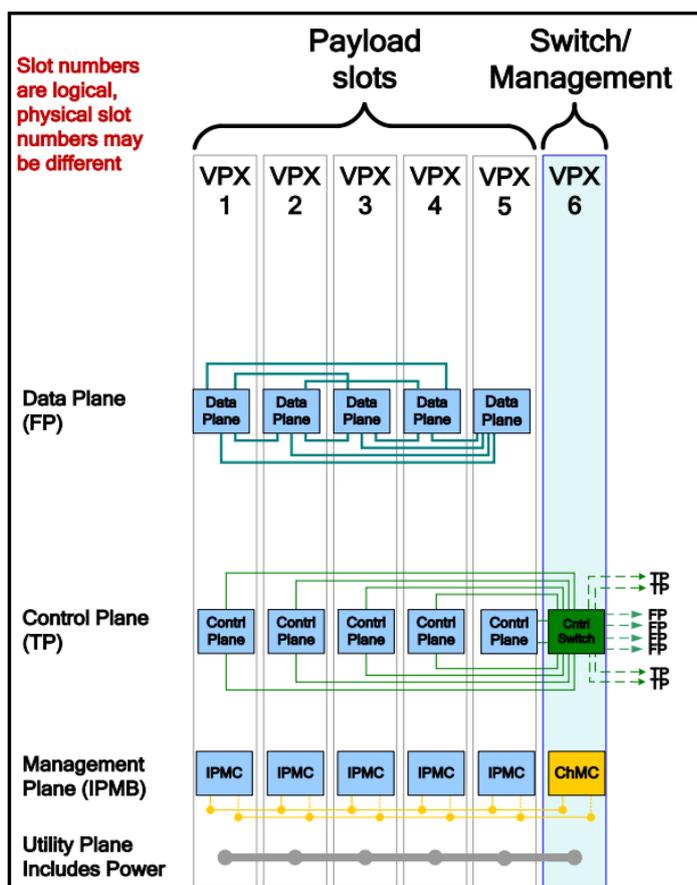
**产品结构尺寸：**



产品 KEY 值

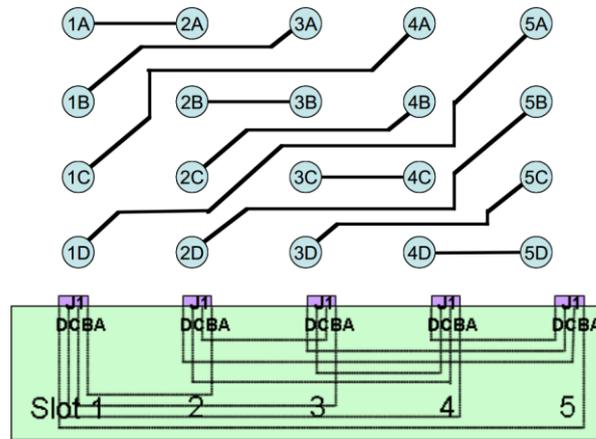
Slot	Key1	Key2	Key3
Slot1	315°	270°	270°
Slot2	315°	315°	270°
Slot3	315°	0°	270°
Slot4	315°	45°	270°
Slot5	315°	90°	270°
Slot6	315°	270°	315°

背板拓扑结构

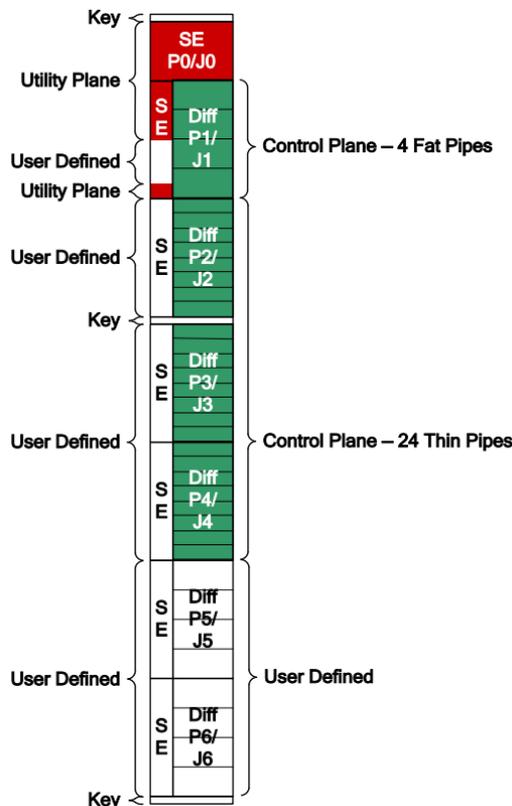


Profile name	Mechanical		Slot Profiles and Section		Channel Gbaud Rate	
	Pitch (in)	RTM Conn	Payload	Switch	Control Plane	Data Plane
BKP6-DIS06-11.2.10-1	1.0	VITA 46.10	SLT6-PAY-4F2T-10.2.2	SLT6-SWH-4F24T-10.4.4	1.25	3.125
BKP6-DIS06-11.2.10-2	1.0	VITA 46.10	SLT6-PAY-4F2T-10.2.2	SLT6-SWH-4F24T-10.4.4	1.25	5.0
BKP6-DIS06-11.2.10-3	1.0	VITA 46.10	SLT6-PAY-4F2T-10.2.2	SLT6-SWH-4F24T-10.4.4	1.25	6.25

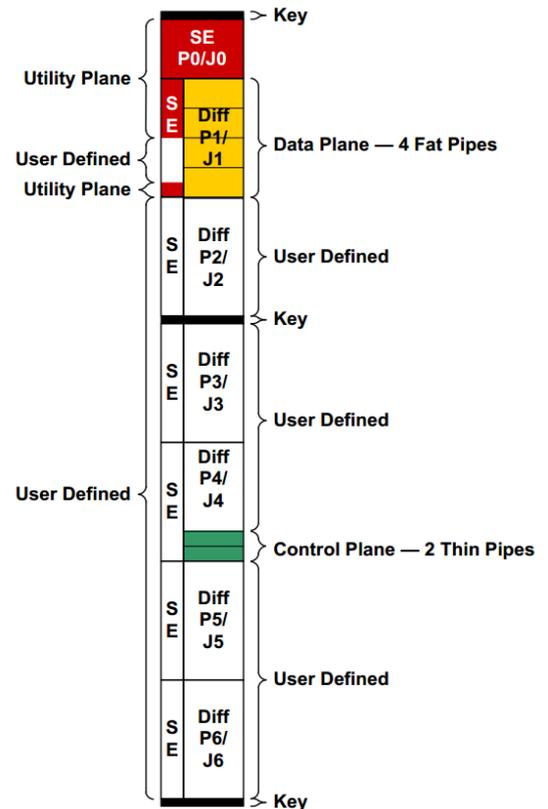
**P1/J1 网络连接关系 Data Plane-4 Fat Pipes**



**Switch Slot Profile SLT6-SWH-4F24T**



**Payload Slot Profile SLT6-PAY-4F2T**



## J0 Signal Assignments

	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	Vs1	Vs1	Vs1	Vs1	No Pad*	Vs2	Vs2	Vs2	Vs2
2	Vs1	Vs1	Vs1	Vs1	No Pad*	Vs2	Vs2	Vs2	Vs2
3	Vs3	Vs3	Vs3	Vs3	No Pad*	Vs3	Vs3	Vs3	Vs3
4	GND	SM2	SM3	GND	-12V_Aux	GND	SYSRESET*	NVMRO	GND
5	GND	GAP*	GA4*	GND	3.3V_Aux	GND	SM0	SM1	GND
6	GND	GA3*	GA2*	GND	+12V_Aux	GND	GA1*	GA0*	GND
7	TCK	GND	GND	TDO	TDI	GND	GND	TMS	TRST*
8	GND	REF_CLK-	REF_CLK+	GND	GND	AUX_CLK-	AUX_CLK+	GND	GND

## J1/P1 Signal Assignments

Plug-In Module P1	Row G	Row F	Row E		Row D	Row C	Row B		Row A	
			Even	Odd			Even	Odd		
Bplane J1	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a	
Data Plane Port 1	1	GDiscrete1	GND	GND-J1	DP01-T0-	DP01-T0+	GND	GND-J1	DP01-R0-	DP01-R0+
	2	GND	DP01-T1-	DP01-T1+	GND-J1	GND	DP01-R1-	DP01-R1+	GND-J1	GND
	3	P1-VBAT	GND	GND-J1	DP01-T2-	DP01-T2+	GND	GND-J1	DP01-R2-	DP01-R2+
	4	GND	DP01-T3-	DP01-T3+	GND-J1	GND	DP01-R3-	DP01-R3+	GND-J1	GND
Data Plane Port 2	5	SYS_CON*	GND	GND-J1	DP02-T0-	DP02-T0+	GND	GND-J1	DP02-R0-	DP02-R0+
	6	GND	DP02-T1-	DP02-T1+	GND-J1	GND	DP02-R1-	DP02-R1+	GND-J1	GND
	7	Reserved	GND	GND-J1	DP02-T2-	DP02-T2+	GND	GND-J1	DP02-R2-	DP02-R2+
	8	GND	DP02-T3-	DP02-T3+	GND-J1	GND	DP02-R3-	DP02-R3+	GND-J1	GND
Data Plane Port 3	9	UD	GND	GND-J1	DP03-T0-	DP03-T0+	GND	GND-J1	DP03-R0-	DP03-R0+
	10	GND	DP03-T1-	DP03-T1+	GND-J1	GND	DP03-R1-	DP03-R1+	GND-J1	GND
	11	UD	GND	GND-J1	DP03-T2-	DP03-T2+	GND	GND-J1	DP03-R2-	DP03-R2+
	12	GND	DP03-T3-	DP03-T3+	GND-J1	GND	DP03-R3-	DP03-R3+	GND-J1	GND
Data Plane Port 4	13	UD	GND	GND-J1	DP04-T0-	DP04-T0+	GND	GND-J1	DP04-R0-	DP04-R0+
	14	GND	DP04-T1-	DP04-T1+	GND-J1	GND	DP04-R1-	DP04-R1+	GND-J1	GND
	15	Maskable Reset*	GND	GND-J1	DP04-T2-	DP04-T2+	GND	GND-J1	DP04-R2-	DP04-R2+
	16	GND	DP04-T3-	DP04-T3+	GND-J1	GND	DP04-R3-	DP04-R3+	GND-J1	GND

## J2/P2 Signal Assignments

Plug-In Module P2	Row G	Row F	Row E		Row D	Row C	Row B		Row A			
			Even	Odd			Even	Odd				
Backplane J2	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a			
X16 using [1:15:0]	x8 using [7:0]	x4 using [3:0]	1	UD	GND	GND-J2	EP00-T-	EP00-T+	GND	GND-J2	EP00-R-	EP00-R+
			2	GND	EP01-T-	EP01-T+	GND-J2	GND	EP01-R-	EP01-R+	GND-J2	GND
			3	UD	GND	GND-J2	EP02-T-	EP02-T+	GND	GND-J2	EP02-R-	EP02-R+
			4	GND	EP03-T-	EP03-T+	GND-J2	GND	EP03-R-	EP03-R+	GND-J2	GND
		x4 using [7:4]	5	UD	GND	GND-J2	EP04-T-	EP04-T+	GND	GND-J2	EP04-R-	EP04-R+
			6	GND	EP05-T-	EP05-T+	GND-J2	GND	EP05-R-	EP05-R+	GND-J2	GND
			7	UD	GND	GND-J2	EP06-T-	EP06-T+	GND	GND-J2	EP06-R-	EP06-R+
			8	GND	EP07-T-	EP07-T+	GND-J2	GND	EP07-R-	EP07-R+	GND-J2	GND
	x8 using [15:8]	x4 using [11:8]	9	UD	GND	GND-J2	EP08-T-	EP08-T+	GND	GND-J2	EP08-R-	EP08-R+
			10	GND	EP09-T-	EP09-T+	GND-J2	GND	EP09-R-	EP09-R+	GND-J2	GND
			11	UD	GND	GND-J2	EP10-T-	EP10-T+	GND	GND-J2	EP10-R-	EP10-R+
			12	GND	EP11-T-	EP11-T+	GND-J2	GND	EP11-R-	EP11-R+	GND-J2	GND
		x4 using [15:12]	13	UD	GND	GND-J2	EP12-T-	EP12-T+	GND	GND-J2	EP12-R-	EP12-R+
			14	GND	EP13-T-	EP13-T+	GND-J2	GND	EP13-R-	EP13-R+	GND-J2	GND
			15	UD	GND	GND-J2	EP14-T-	EP14-T+	GND	GND-J2	EP14-R-	EP14-R+
			16	GND	EP15-T-	EP15-T+	GND-J2	GND	EP15-R-	EP15-R+	GND-J2	GND

### J4/P4 Signal Assignments

Plug-In Mod P4	Row G	Row F	Row E		Row D	Row C	Row B		Row A
			Even	Odd			Even	Odd	
Bplane J4	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	UD	GND	GND-J4	UD	UD	GND	GND-J4	UD	UD
2	GND	UD	UD	GND-J4	GND	UD	UD	GND-J4	GND
3	UD	GND	GND-J4	UD	UD	GND	GND-J4	UD	UD
4	GND	UD	UD	GND-J4	GND	UD	UD	GND-J4	GND
5	UD	GND	GND-J4	UD	UD	GND	GND-J4	UD	UD
6	GND	UD	UD	GND-J4	GND	UD	UD	GND-J4	GND
7	UD	GND	GND-J4	UD	UD	GND	GND-J4	UD	UD
8	GND	UD	UD	GND-J4	GND	UD	UD	GND-J4	GND
9	UD	GND	GND-J4	UD	UD	GND	GND-J4	UD	UD
10	GND	UD	UD	GND-J4	GND	UD	UD	GND-J4	GND
11	UD	GND	GND-J4	UD	UD	GND	GND-J4	UD	UD
12	GND	UD	UD	GND-J4	GND	UD	UD	GND-J4	GND
13	UD	GND	GND-J4	CPTp02-DB-	CPTp02-DB+	GND	GND-J4	CPTp02-DA-	CPTp02-DA+
14	GND	CPTp02-DD-	CPTp02-DD+	GND-J4	GND	CPTp02-DC-	CPTp02-DC+	GND-J4	GND
15	UD	GND	GND-J4	CPTp01-DB-	CPTp01-DB+	GND	GND-J4	CPTp01-DA-	CPTp01-DA+
16	GND	CPTp01-DD-	CPTp01-DD+	GND-J4	GND	CPTp01-DC-	CPTp01-DC+	GND-J4	GND

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### J2/P2 Signal Assignments for Switch Slot 6

Plug-In Module P2	Row G	Row F	Row E		Row D	Row C	Row B		Row A
			Even	Odd			Even	Odd	
Bplane J2	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	UD	GND	GND-J2	CPTp01-DB-	CPTp01-DB+	GND	GND-J2	CPTp01-DA-	CPTp01-DA+
2	GND	CPTp01-DD-	CPTp01-DD+	GND-J2	GND	CPTp01-DC-	CPTp01-DC+	GND-J2	GND
3	UD	GND	GND-J2	CPTp02-DB-	CPTp02-DB+	GND	GND-J2	CPTp02-DA-	CPTp02-DA+
4	GND	CPTp02-DD-	CPTp02-DD+	GND-J2	GND	CPTp02-DC-	CPTp02-DC+	GND-J2	GND
5	UD	GND	GND-J2	CPTp03-DB-	CPTp03-DB+	GND	GND-J2	CPTp03-DA-	CPTp03-DA+
6	GND	CPTp03-DD-	CPTp03-DD+	GND-J2	GND	CPTp03-DC-	CPTp03-DC+	GND-J2	GND
7	UD	GND	GND-J2	CPTp04-DB-	CPTp04-DB+	GND	GND-J2	CPTp04-DA-	CPTp04-DA+
8	GND	CPTp04-DD-	CPTp04-DD+	GND-J2	GND	CPTp04-DC-	CPTp04-DC+	GND-J2	GND
9	UD	GND	GND-J2	CPTp05-DB-	CPTp05-DB+	GND	GND-J2	CPTp05-DA-	CPTp05-DA+
10	GND	CPTp05-DD-	CPTp05-DD+	GND-J2	GND	CPTp05-DC-	CPTp05-DC+	GND-J2	GND
11	UD	GND	GND-J2	CPTp06-DB-	CPTp06-DB+	GND	GND-J2	CPTp06-DA-	CPTp06-DA+
12	GND	CPTp06-DD-	CPTp06-DD+	GND-J2	GND	CPTp06-DC-	CPTp06-DC+	GND-J2	GND
13	UD	GND	GND-J2	CPTp07-DB-	CPTp07-DB+	GND	GND-J2	CPTp07-DA-	CPTp07-DA+
14	GND	CPTp07-DD-	CPTp07-DD+	GND-J2	GND	CPTp07-DC-	CPTp07-DC+	GND-J2	GND
15	UD	GND	GND-J2	CPTp08-DB-	CPTp08-DB+	GND	GND-J2	CPTp08-DA-	CPTp08-DA+
16	GND	CPTp08-DD-	CPTp08-DD+	GND-J2	GND	CPTp08-DC-	CPTp08-DC+	GND-J2	GND

### J3/P3 Signal Assignments for Switch Slot 6

Plug-In Module P3	Row G	Row F	Row E		Row D	Row C	Row B		Row A
			Even	Odd			Even	Odd	
Bplane J3	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	UD	GND	GND-J3	CPtp09-DB-	CPtp09-DB+	GND	GND-J3	CPtp09-DA-	CPtp09-DA+
2	GND	CPtp09-DD-	CPtp09-DD+	GND-J3	GND	CPtp09-DC-	CPtp09-DC+	GND-J3	GND
3	UD	GND	GND-J3	CPtp10-DB-	CPtp10-DB+	GND	GND-J3	CPtp10-DA-	CPtp10-DA+
4	GND	CPtp10-DD-	CPtp10-DD+	GND-J3	GND	CPtp10-DC-	CPtp10-DC+	GND-J3	GND
5	UD	GND	GND-J3	CPtp11-DB-	CPtp11-DB+	GND	GND-J3	CPtp11-DA-	CPtp11-DA+
6	GND	CPtp11-DD-	CPtp11-DD+	GND-J3	GND	CPtp11-DC-	CPtp11-DC+	GND-J3	GND
7	UD	GND	GND-J3	CPtp12-DB-	CPtp12-DB+	GND	GND-J3	CPtp12-DA-	CPtp12-DA+
8	GND	CPtp12-DD-	CPtp12-DD+	GND-J3	GND	CPtp12-DC-	CPtp12-DC+	GND-J3	GND
9	UD	GND	GND-J3	CPtp13-DB-	CPtp13-DB+	GND	GND-J3	CPtp13-DA-	CPtp13-DA+
10	GND	CPtp13-DD-	CPtp13-DD+	GND-J3	GND	CPtp13-DC-	CPtp13-DC+	GND-J3	GND
11	UD	GND	GND-J3	CPtp14-DB-	CPtp14-DB+	GND	GND-J3	CPtp14-DA-	CPtp14-DA+
12	GND	CPtp14-DD-	CPtp14-DD+	GND-J3	GND	CPtp14-DC-	CPtp14-DC+	GND-J3	GND
13	UD	GND	GND-J3	CPtp15-DB-	CPtp15-DB+	GND	GND-J3	CPtp15-DA-	CPtp15-DA+
14	GND	CPtp15-DD-	CPtp15-DD+	GND-J3	GND	CPtp15-DC-	CPtp15-DC+	GND-J3	GND
15	UD	GND	GND-J3	CPtp16-DB-	CPtp16-DB+	GND	GND-J3	CPtp16-DA-	CPtp16-DA+
16	GND	CPtp16-DD-	CPtp16-DD+	GND-J3	GND	CPtp16-DC-	CPtp16-DC+	GND-J3	GND

### J4/P4 Signal Assignments for Switch Slot 6

Plug-In Module P4	Row G	Row F	Row E		Row D	Row C	Row B		Row A
			Even	Odd			Even	Odd	
Bplane J4	Row i	Row h	Row g	Row f	Row e	Row d	Row c	Row b	Row a
1	UD	GND	GND-J4	CPtp17-DB-	CPtp17-DB+	GND	GND-J4	CPtp17-DA-	CPtp17-DA+
2	GND	CPtp17-DD-	CPtp17-DD+	GND-J3	GND	CPtp17-DC-	CPtp17-DC+	GND-J4	GND
3	UD	GND	GND-J4	CPtp18-DB-	CPtp18-DB+	GND	GND-J4	CPtp18-DA-	CPtp18-DA+
4	GND	CPtp18-DD-	CPtp18-DD+	GND-J4	GND	CPtp18-DC-	CPtp18-DC+	GND-J4	GND
5	UD	GND	GND-J4	CPtp19-DB-	CPtp19-DB+	GND	GND-J3	CPtp19-DA-	CPtp19-DA+
6	GND	CPtp19-DD-	CPtp19-DD+	GND-J4	GND	CPtp19-DC-	CPtp19-DC+	GND-J4	GND
7	UD	GND	GND-J4	CPtp20-DB-	CPtp20-DB+	GND	GND-J4	CPtp20-DA-	CPtp20-DA+
8	GND	CPtp20-DD-	CPtp20-DD+	GND-J4	GND	CPtp20-DC-	CPtp20-DC+	GND-J4	GND
9	UD	GND	GND-J4	CPtp21-DB-	CPtp21-DB+	GND	GND-J4	CPtp21-DA-	CPtp21-DA+
10	GND	CPtp21-DD-	CPtp21-DD+	GND-J4	GND	CPtp21-DC-	CPtp21-DC+	GND-J4	GND
11	UD	GND	GND-J4	CPtp22-DB-	CPtp22-DB+	GND	GND-J4	CPtp22-DA-	CPtp22-DA+
12	GND	CPtp22-DD-	CPtp22-DD+	GND-J4	GND	CPtp22-DC-	CPtp22-DC+	GND-J4	GND
13	UD	GND	GND-J4	CPtp23-DB-	CPtp23-DB+	GND	GND-J4	CPtp23-DA-	CPtp23-DA+
14	GND	CPtp23-DD-	CPtp23-DD+	GND-J4	GND	CPtp23-DC-	CPtp23-DC+	GND-J4	GND
15	UD	GND	GND-J4	CPtp24-DB-	CPtp24-DB+	GND	GND-J4	CPtp24-DA-	CPtp24-DA+
16	GND	CPtp24-DD-	CPtp24-DD+	GND-J4	GND	CPtp24-DC-	CPtp24-DC+	GND-J4	GND

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